

**2018**

**CBCS**

**1st Semester**

**ELECTRONICS**

**PAPER—GE1P**

**(Honours)**

**(Practical)**

*Full Marks : 20*

*Time : 2 Hours*

*The figures in the right-hand margin indicate full marks.*

*Candidates are required to give their answers in their own words as far as practicable.*

*Illustrate the answers wherever necessary.*

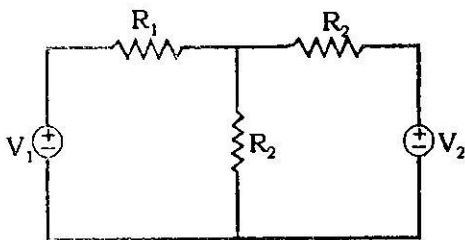
*Electronic Circuits and PCB Designing Lab.*

*Answer any one question selecting by a lucky draw.*

1. Verify Thevenis theorem of a given circuit.

*(The circuit to the provided during exam)*

2. Verify super position theorem of the following circuit.



- (The values of  $V_1$ ,  $V_2$ ,  $R_1$ ,  $R_2$  and  $R_3$  will be provided during exam)
- Design half wave rectifier circuit & study wave form with & without capacitor filter/
  - Design full wave rectifier circuit and study wave form using with and without capacitor filter.
  - Use zener diode as voltage regulator and study its low regulator.
  - Design a CE amplifier of gain.....(gain is to be supplied during exam).

Distribution of Marks

<b>Experiment :</b>	<b>15 Marks</b>
<b>Laboratory Note Book :</b>	<b>02 Marks</b>
<b>Viva Voce :</b>	<b>03 Marks</b>
<b>Total</b>	<b>20 Marks</b>