2018

CBCS

3rd Semester

ELECTRONICS

PAPER—C6P

(Honours)

(Practical)

Full Marks : 20

Time : 2 Hours

The figures in the margin indicate full marks.

Candidates are required to give their answers in their own words as far as practicable.

Illustrate the answers wherever necessary.

Electronic Circuits Lab.

1. Study the function of half wave rectifier and Full wave rectifier. Show the output wave forms in both cases.

2. Design a regulated power supply using zener diode and capacitive filter and study its performance.

3. Design a 5V or 9V regulated power supply using 78xx IC and study its load regulation characteristic.

4. Study the operation of clipping and clamping circuits and show the nature of input and output waveforms.

5. Design CE transistor amplifiers with fixed bias, Voltage divider bias and collector to base bios and study their operations.

6. Design class A, class B and Class C power amplifier and study their operations.

7. Design a Hartley's oscillator and study its operation.

8. Design a Colpitt's oscillator and study its operation.

9. Design a RC phase shift oscillator and study its operation.

10. Design a common source FET amplifier and study its frequency response charactristic.

(Continued)

Distribution of Marks :

Experiment : 15 marks

Laboratory Note Book : 02 marks

Viva-Voce : 03 marks

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