

**2018**  
**CBCS**  
**1st Semester**  
**ELECTRONICS**  
**PAPER—CIP**  
**(Honours)**  
**(Practical)**

*Full Marks : 20*

*Time : 1 Hours*

*The figures in the right-hand margin indicate full marks.*

*Candidates are required to give their answers in their own words as far as practicable.*

*Illustrate the answers wherever necessary.*

***Basic Circuit Theory and Network Analysis***

Answer to any one question  
selecting by a lucky draw.

1. Design a low pass RC filter off cut off frequency and study its frequency response.

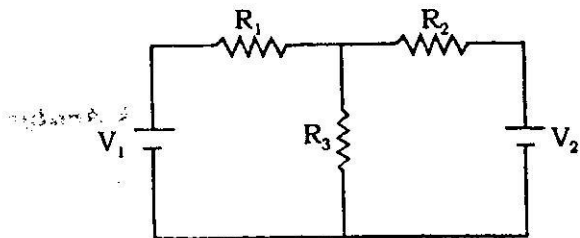
*(Cut off frequency to be supplied during examination)*

*(Turn Over)*

2. Design a high pass RC filter of frequency.....and study its frequency response.

(Cut off frequency to be supplied during examination)

3. Verify hevensis theorem by using resistive wheat stone Bridge Network (The values of resistance is to be supplied during examination).
4. Verify Norton's theorem by using resistive wheatstone bridge network. (The values of resistance is to be supplied during examination).
5. Verify Supea position theorem of the following circuit.



(The values of resistance and voltage  $r$  to be supplied during examination)

- (a) Resonance curve.
- (b) Determine the Q-factor of the circuit.

**Distribution of Marks**

<b>Experiment :</b>	<b>15 Marks</b>
<b>Laboratory Note Book :</b>	<b>02 Marks</b>
<b>Viva Voce :</b>	<b>03 Marks</b>
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<b>Total</b>	<b>20 Marks</b>
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