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Full Marks: 40

UG/3rd Sem/ELEC/(H)/T/19

2019

B.Sc.

3rd Semester Examination

ELECTRONICS (Honours)

Paper - C 6-T

(Electronic circuits)

The question are of equal value for any group/half. The figures in the margin indicate full Marks. Candidates are required to give their answers in their own words as far as parctiable. Illustrate the answers wherever necessary.

1. Answer any five questions:

5×2

Time: 2 Hours

a) Define O point in case of a transistor.

. 2

- b) Write down two advantages of full bridge rectifier circuit over fullwave centre-tap rectifier. 2
- c) Define stabilization of a transistor. Mention two techniques of stabilization.
- d) Compare performance of different power amplifier in terms of their efficiencies.

[Turn Over]

e)	What is the main difference of depletion and enhancement type MOSFET in respect to their construction.?	
f)	Mention two criterion of a circuit being a oscillator. 2	
g)	Write down two advantages of CMOS amplifier curcuit.	
h)	What is Miller's effect ?	
2. Ansv	wer any four questions: $4 \times 5 = 20$	
a)	Explain the operation of N-channel enhancement type MOSFET with suitable circuit diagram. 5	
b)	Explain the operation of full-wave bridge rectifier circuit using suitable circuit diagram. Give input-output waveform and calculate ripple factor. 2+3	
c)	How does BJT work as an amplifier and switch? Explain it using its Voltage transfer curve. 2+3	
d)	Define biasing of a transistor. What is stabilization? Why stabilization is required for a transistor. 1+1+3	
e)	Differentiate between positive and negative feedback. Mention different types of negative feedback circuit and compare between them. 2+3	

f) Explain working principle of shunt regulated power

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supply using BJT.

- a) Explain common source amplifier circuit using proper circuit diagram (use small signal equivalent circuit).
 Deduce input resistance, output resistance and voltage gain.
- b) Explain principle of operation of class-C power amplifier and clculate its efficiency. 5+5