2019

3rd Semester Examination

ELECTRONICS (Honours)

Paper - C6P

(Electronic Circuit Lab)

Full Marks : 20 Time : 3 Hours

The figures in the margin indicate full marks.
Candidates are required to give their answers in their own words as far as practicable.

Answer any one questions selecting it by a lucky draw.

1. Design a full wave fridge rectifier circuit and study its input /output waveform.
2. Design a regulated power supply using zener diode and capacitive filter and study its performance.
3. Design a 5V DC regulated power supply and measure its load regulation.
4. Design a clamper circuit and study its input and output characteristics for a sinusotdal wave.

[Turn Over]

5. Design a voltage divider bias and fixed its Q-point.
6. Design class C power amplifier and study its performance.
7. Design a Hartley's oscillator and find its output frequency.
8. Design a colpitt's oscillator and find its oscillating frequency.
9. Design a RC phase shift oscillator circuit and find its output frequency.
10. Design a common source FBT amplifier and study its frequency response characteristics.

Distribution of Marks:

Experiment	: 15 Marks
(Th/Ckt = 3, data recording = 8, plotting of graph and calculations = 4)	
Laboratory Note Book	: 02 Marks
Viva-voce	: 03 Marks
Total	: 20 Marks