2019

BCA

2nd Semester Examination

Computer Organization & Architecture

Paper - 1201

Full Marks - 70

Time: 3 Hours

The figures in the margin indicate full marks.

Candidates are required to give their answers in their own words as far as practicable.

Question No. 1 and any four from the rest.

- Answer any five questions : 2×5
 - (a) What are the limitations of CISC architecture processor?
 - (b) Why NAND gate is called universal gate?
 - (c) State and prove distributive law.
 - (d) What do you mean by micro instruction?

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What is addrassing mode of an instruction? (e) (f) What is bus? (g) What is tri state device? (h) Define flash memory. 2. (a) Evalute the following statement $X = \frac{A - B + C^*(D^*E - F)}{G + H^*K}$ Using a general register computer with (i) three address instructions. (ii) Using zero address instructions. (iii) Using one address instructions. Explain different type of addressing modes (b) of instruction. 6 Compare horizontal and vertical micro 3. (a) instruction. 6 (b) A digital computer has a common bus system for 16 registers of 32 bit each. The bus is constructed with multiplexers. 9 How many selection inputs are there in (i) each multiplexer? Contd. 2 BCA (1201)

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- 7. (a) How associative memory differs from any other memory?
 - (b) Explain the hardware organization and working procedure of associative memory with the help of a block diagram.5+5

[Internal Assessment – 30 marks]