

VIDYASAGAR UNIVERSITY

MIDNAPORE - 721 102

Ref. No. VU/R/Ph.D./257 /14

Date -14.05.2014

Dated: June 06, 2014

From : The Registrar(Actg.)

Vidyasagar University

To : The Secretary, Ph.D. Committee

Department of *Electronics*

Vidyasagar University

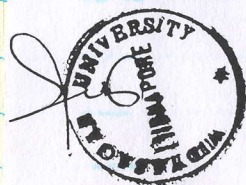
Dear Sir/Madam,

You are requested to send the final moderated questions of the Written Admission Test for the Ph.D. Programme, 2014 to the Controller of Examinations in a sealed cover on or before 06.06.2014. All the members of the Ph.D. Committee concerned will set and moderate the questions. The pattern of questions for the Written Admission Test is being enclosed herewith for your ready reference.

Yours sincerely,

(Dr. J. K. Nandi)

Registrar(Actg.)



S. K. Nandi
14/05/14
S. K. Nandi
15-5-14

R. Ghosh
15.05.14
S. Pramanik
15/05/2014
A. K. Mallik
15.05.14

Ref. No.: VU/R/Ph.D./257/14, dated 14.05.2014, Issued by the Registrar (Actg.)

Members Present:

1. Prof. D. P. Bhattacharya: *D. P. Bhattacharya* 06.06.2014
2. Prof. Partha Chaudhuri: *Partha Chaudhuri* 06/06/2014
3. Prof. Radha Raman Pal: *R. Pal* 6.6.14.
4. Dr. Subhas Chandra Saha: *Subhas chandra Saha* 06/06/2014

Resolutions of the PhD Committee Meeting held on June 06, 2014

A meeting of the PhD committee (Electronics) has been held on June 06, 2014 in the department of Electronics, Vidyasagar University. at 12.00 noon to moderate the question papers of the Written Admission Test in the Subject Electronics, for the PhD Programme, 2014.

The moderated question paper is submitted to the controller of Examinations, Vidyasagar University by a sealed envelope.

There was no other matter.

D. P. Bhattacharya 06.06.2014
Partha Chaudhuri 06/06/14
R. Pal
6.6.14
Subhas chandra Saha
06/06/2014

12/18