

2025

M.Sc. 1st Semester Examination

ELECTRONICS

Paper : ELCC405X0

[Microprocessor]

Full Marks : 25

Time : One Hours

The figures in the margin indicate full marks.

*Candidates are required to give their answers
in their own words as far as practicable.*

Group - A

Answer any *two* questions : $2 \times 2 = 4$

1. Calculate the number of memory chips needed to design 8k-byte memory if the memory chip size is 1024×1 .
2. If the 8085 adds 87H and 79H, specify the contents of the accumulator and status of S, Z and CY flags.
 $\frac{1}{2} \times 4$
3. How is the physical address generated in the 8086 microprocessor?
4. What is a microcontroller?

P.T.O.

(2)

Group - B

Answer any *two* questions : $4 \times 2 = 8$

5. Mention different addressing modes of 8085 microprocessor and give one example of each. $2+2$
6. Compare memory-mapped I/O and peripheral I/O.
7. Explain the pipeline architecture in 8086 micro-processor.
8. Write down the features of Intel 8051.

Group - C

Answer any *one* question : $8 \times 1 = 8$

9. Draw and discuss the architecture of 8085 micro-processor. $4+4$
10. Draw and describe the internal block diagram of 8086 microprocessor. $4+4$

Internal Assessment : 5 marks
