

VIDYASAGAR UNIVERSITY

MIDNAPORE - 721 102

Ref.No. : VU/R/Ph.D/ 581/14

Date : 27.10.2014

NOTIFICATION FOR INTERVIEW OF Ph.D PROGRAMME, 2014 IN ELECTRONICS

It is for information to the qualified candidates at the Written Admission Test and the candidates Exempted from Written Admission Test in the Ph.D. Programme, 2014 in Electronics that the Interview for Ph.D. Programme, 2014 will be held on **01.11.2014** at 11-00 am at the Department of Electronics, Vidyasagar University. All concerned are requested to report to the department by 10-30 am on the schedule date.

(Dr.J.K.Nandi)
Registrar(Actg.)

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Dated: November 01, 2014

Ref. No.: VU/R/Ph.D/581/14, dated 27.10.2014,

Issued by the Registrar (Actg.)

Members Present:

1. Hon'ble Vice-Chancellor:
2. Dean, Faculty of Science: *[Signature]* 01/11/14
3. Prof. D. P. Bhattacharya: *D.P. Bhattacharya* 01.11.2014
4. Prof. Partha Chaudhuri: *Partha Chaudhuri* 01/11/14
5. Prof. Radha Raman Pal: *Radha* 1.11.14
6. Dr. Subhas Chandra Saha: *Subhas chandra Saha* 01/11/2014

Resolutions of the PhD Committee Meeting held on November 01, 2014

A meeting of the PhD committee (Electronics) has been held on November 01, 2014 in the department of Electronics, Vidyasagar University. at 12.00 noon to conduct the Interview for the for the PhD Programme, 2014 in Electronics. 03 (Three) candidates are enlisted for the interview. 01 (One) out of the 03 (three) has appeared in the Interview.

The Attendance Record and the Award List of the interview are prepared, which are given in a separate sealed envelope (Confidentially enclosed).

There was no other matter.

D.P. Bhattacharya 01.11.2014
Partha Chaudhuri 01/11/14

Radha
1.11.14

Subhas chandra Saha
01/11/2014

[Signature]
01/11/14

12/18